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The UK's leading health and social care software and services company.

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[Best Practice to Synchronize Modules - SystemC Language ...](#)

Hi, I created a pair of modules in SystemC. The purpose is to model a data transfer with one side being the source and the other side being the sink.

[ESL | Electronic System Level Design - Mentor Graphics](#)

ESL addresses the problem of advanced designs that have become too massive and complex by elevating design & verification to a higher level of abstraction.

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VHDL Designer's Guide - Doulos

Great VHDL Stuff For You VHDL PaceMaker. VHDL PaceMaker is a self-teach tutorial that gives you a great foundation in the basics of the VHDL language.

Simics Full System Simulator - Wind River

Wind River Simics simulates systems-from the smallest to the most complex-so you can adopt new development techniques that are simply not possible with physical hardware.

The CBMC Homepage

CBMC is a Bounded Model Checker for C and C++ programs. It supports C89, C99, most of C11 and most compiler extensions provided by gcc and Visual Studio.

Synopsys - Wikipedia

Synopsys is an American company. Synopsys' first and best-known product is Design Compiler, a logic-synthesis tool. Synopsys offers a wide range of other products.